Refine Search

Search Results -

| Term | Documents |
|---|-----------|
| (35 AND 6).PGPB,USPT,EPAB,JPAB,DWPI,TDBD. | 0 |
| (L6 AND L35).PGPB,USPT,EPAB,JPAB,DWPI,TDBD. | 0 |

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
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EPO Abstracts Database
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IBM Technical Disclosure Bulletins

L37

Refine Search
Recall Text

Search History

DATE: Tuesday, October 24, 2006 Purge Queries Printable Copy Create Case

| Set Name side by side | | Hit Count | Set Name result set |
|--------------------------|--|-----------|---------------------|
| DB=PG | PB, USPT, EPAB, JPAB, DWPI, TDBD; THES=ASSIGNEE; PLUR=YE | S; OP=ADJ | |
| <u>L37</u> | 16 and 135 | 0 | <u>L37</u> |
| <u>L36</u> | 5778882.pn. | 2 | <u>L36</u> |
| DB=US | PT; THES=ASSIGNEE; PLUR=YES; OP=ADJ | | |
| <u>L35</u> | (6057758 5461365 6052693 5778882 5136285)![PN] | 5 | <u>L35</u> |
| <u>L34</u> | ("6222449")[PN] | 1 | <u>L34</u> |
| DB=PG | PB, USPT, EPAB, JPAB, DWPI, TDBD; THES=ASSIGNEE; PLUR=YE | S; OP=ADJ | |
| <u>L33</u> | L31 and 126 | 0 | <u>L33</u> |
| <u>L32</u> | 6903681.pn. | 4 | <u>L32</u> |
| DB=US | PT; THES=ASSIGNEE; PLUR=YES; OP=ADJ | | |
| <u>L31</u> | ("6222449")[URPN] | 12 | <u>L31</u> |
| DB=PG | PB, USPT, EPAB, JPAB, DWPI, TDBD; THES=ASSIGNEE; PLUR=YE | S; OP=ADJ | |
| <u>L30</u> | 6222449.pn. | 2 | <u>L30</u> |
| <u>L29</u> | l21 and L28 | 1 | <u>L29</u> |

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| <u>L28</u> | L2 and 125 | 24 | <u>L28</u> |
|------------|---|---------|------------|
| <u>L27</u> | 12 and L26 | 2 | <u>L27</u> |
| <u>L26</u> | 11 and L25 | 9 | <u>L26</u> |
| <u>L25</u> | catch\$ near2 fish | 3930 | <u>L25</u> |
| <u>L24</u> | 112 and L23 | 4 | <u>L24</u> |
| <u>L23</u> | 119 and 121 and L22 | 14070 | <u>L23</u> |
| <u>L22</u> | switch or keys or keypad or touchpad | 2678508 | <u>L22</u> |
| <u>L21</u> | communication near2 port | 78824 | <u>L21</u> |
| <u>L20</u> | (commuication near2 port) same (vessel near2 navigation) | 0 | <u>L20</u> |
| <u>L19</u> | memory same processor | 383684 | <u>L19</u> |
| <u>L18</u> | L6 and l4 | 2 | <u>L18</u> |
| <u>L17</u> | 20040054821.pn. | 2 | <u>L17</u> |
| <u>L16</u> | 12 and L15 | 1 | <u>L16</u> |
| <u>L15</u> | 112 and 113 | 23 | <u>L15</u> |
| <u>L14</u> | 14 and 112 and L13 | 1 | <u>L14</u> |
| <u>L13</u> | catch near2 information | 340 | <u>L13</u> |
| <u>L12</u> | fish\$ near2 information | 865 | <u>L12</u> |
| <u>L11</u> | 19 and 14 | 1 | <u>L11</u> |
| <u>L10</u> | 17 and L9 | 0 | <u>L10</u> |
| <u>L9</u> | (catch near2 information) or ((by-catch) near2 information) | 340 | <u>L9</u> |
| <u>L8</u> | by catch information or by-catch information | 1 | <u>L8</u> |
| <u>L7</u> | data near2 switch\$ near2 memory | 2897 | <u>L7</u> |
| <u>L6</u> | 11 and L5 | 671 | <u>L6</u> |
| <u>L5</u> | 12 and 13 | 10141 | <u>L5</u> |
| <u>L4</u> | vessel near2 navigation | 737 | <u>L4</u> |
| <u>L3</u> | communication near2 port | 78824 | <u>L3</u> |
| <u>L2</u> | memory near3 processor | 187563 | <u>L2</u> |
| <u>L1</u> | data near2 acquisition | 65501 | <u>L1</u> |

END OF SEARCH HISTORY